

Agenda Basic SI Training

Morning 1

Welcome and introduction

08.30 – 08.45 Introduction of Signal Integrity (SI)

08.45 – 10.30 SI Principles explained Easy (with examples)

Problems and Definitions

Break

10.30 – 10.40 Problem prevention

10.40 – 10.50 Principle of the '4T' solution

Break

10.55 – 11.30 3 step design strategies to optimize SI

Board Stackup design (Lab)

11.30 – 12.30 Pre-Layout Simulation (Lab)

Morning 2

08.30 – 10.30 Post-Layout Simulation (Lab)

Break

10.40 – 12.30 Basic IBIS modeling and timing (Lab)

Morning 3

08.30 – 12.00 Crosstalk Investigation (Lab)

Break

12.15 – 12.30 Integration with Schematic Design and PCB Layout (DEMO)

Reference material to go deeper into SI

Start working on your own design